AIPULNION[®]

DC-DC Converter NN2-XXXXXANR3 Series



Typical Features

- Fixed input voltage, Isolated & unregulated output, Output power 2W
- High Efficiency up to 85%
- Small compact SIP packing
- No external component required
- Isolation Voltage 1500VDC
- ◆ Operating Temperature: -40°C~+85°C
- Plastic Case, meet UL94-V0 standard



Test Condition: Unless otherwise specified, data in the datasheet should be tested under the conditions of inputting nominal voltage, pure resistance rated load and Ta=25°C

Application Field

It could be widely used for instrument, communication, pure digital circuit, general low frequency analog circuit, relay drive circuit, data exchange circuit, etc.

Typical Product List

Madal	Input Voltage Range (VDC)		Output Voltage/ Current (Vo/Io)		Input Current(mA) Nominal Voltage		Max. Capacitiv e Load	Ripple & Noise (Max.)	Efficiency (%)	
Model	No min al	Range	Voltage (VDC)	Current(mA) MAX./Min.	Full load Typ.	No Load Typ.	uF	mVp-p	Min.	Тур.
NN2-05S05ANR3	5	4.5 -	5	400/ 40	472	10	1000	100	79	82
NN2-05S09ANR3		5.5	9	223/ 23	470	10	1000	100	82	85
NN2-12S3V3ANR3	40	10.8-	3.3	400/ 40	472	10	1000	100	79	82
NN2-12S05ANR3	12	13.2	5	400/ 40	472	10	1000	100	78	82
NN2-24S3V3ANR3	04	21.6-	3.3	400/ 40	472	10	1000	100	79	82
NN2-24S05ANR3	24	26.4	5	400/ 40	472	10	1000	100	78	82

In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance recommended equal to 10% nominal power;

Input Specifications								
Item	Test Condition	Min.	Тур.	Max.	Unit			
	3.3Vdc Input	-0.7	-	7				
	5Vdc Input	-0.7	-	9				
Input Overshoot Voltage (1Second.max.)	9Vdc Input	-0.7	-	12	VDC			
(recontainax.)	12Vdc Input	-0.7	-	18				
	15Vdc Input	-0.7	-	21				

Guangzhou Aipu Electron Technology Co., Ltd Add: Building 4, HEDY Park, No.63, Punan Road, Huangpu Dist, Guangzhou, CN. Fax: 86-20-84206762 HOTLINE: 400-811-8032 Email: market@aipu-elec.com Tel: 86-20-84206763 Website: http://en.aipulnion.com/ Guangzhou Aipu Electron Technology Co., Ltd reserves the copyright and right of final interpretation. Version: A/0 Date: 2021-03-09 Page 1 of 5

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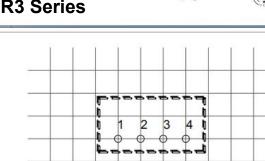
		24Vdc Input -0.7			- 30		
Input Filter			С	Capacitor Filte	r		
Output Specifications							
ITEM	Working Cond	Working Conditions		Тур.	Max.	Unit	
Output Power			0.2		2	W	
Output Voltage Accuracy	Nominal input, F	ull load		±3	±5		
Load Regulation	10% ~ 100% nominal	3.3Vdc output			20		
	load	Other output			15	%	
Line Voltage Regulation	Input Voltage	3.3Vdc output			±1.5		
	Change±1%	Other output			±1.2		
Ripple & Noise (1)	•	Nominal input, full load, 20MHZ bandwidth		70	100	mVp-p	
Temperature Drift Coefficient	100% Full Lo	oad			±0.03	%/°C	
Output Short Circuit Protection Continuous short-circuit protection, self-recovery							
NOTE:① Ripple & Noise Test	ted by twisted-pair methoo	l, for details plea	se che	ck Design an	d Application Ci	rcuit.	
General Specifications							
Switching Frequency	Ту	Typical		100KHz (Typ.)			
Operating Temperature	Refer to Tempera	Refer to Temperature Derating Curve		-40°C ~ +85°C			
Storage Temperature				-55℃ ~ +125℃			
Shell temperature rise during	work Within Temperat	Within Temperature Derating Curve		25℃(Typ.)			
Relative Humidity	No cor	No condensing		5%~95%			
Case Material				Black flame-retardant heat-resistant Plastic(UL94-VC			
Pin Resistance To Weldin Temperature		The solder spot is 1.5mm away from the shell, 10s		1500Vdc			
Isolation Voltage		Test 1 minute, leakage current< 0.5mA		1500Vdc ≤ 0.5mA / 1min			
Isolation Capacitor	Input/Outpu	Input/Output,100KHz/0.1V		20 рҒ (Тур.)			
	MIL-HDBK	MIL-HDBK-217F@25℃		35X10⁵Hrs			
MTBF				1.4g(Typ.)			
MTBF Product Weight					19(1	yp.)	
		5*18*10mm)			43pc	-	

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Printed board vertical view

Lattic spacing:2.54mm(0.1inch)

Packing Dimension

Recommended PCB layout

Packing Code		L x W x H						
А	11.50× 6.0	0 × 10.00mm	0.453 × 0.236 × 0.394inch					
Pin Function								
Single/S)	1	2	3	4				
Single(S)	GND	+Vin	-Vo	+Vo				

Note: if the definition of pin is not in accordance with the model selection manual, please refer to the label on actual item.

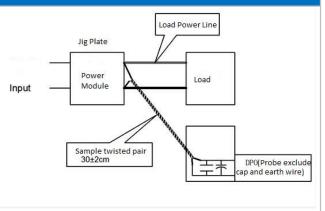
Ripple& Noise Test: (Twisted Pair Method 20MHZ bandwidth)

Test Method:

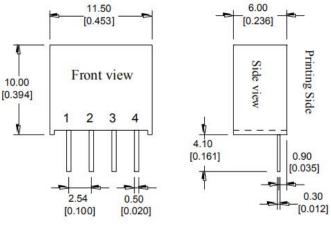
a.12# twisted pair to connect, Oscilloscope bandwidth set as 20MHz, 100M bandwidth probe, terminated with 0.1uF polypropylene capacitor and 10uF high frequency low resistance electrolytic capacitor in parallel, oscilloscope set as Sample pattern.

b. Input terminal connect to power supply, output terminal connect to electronic load through jig plate, Use 30cm±2 cm sampling line, Power line selected from corresponding diameter wire with insulation according to the flow of output current.

Temperature Curve



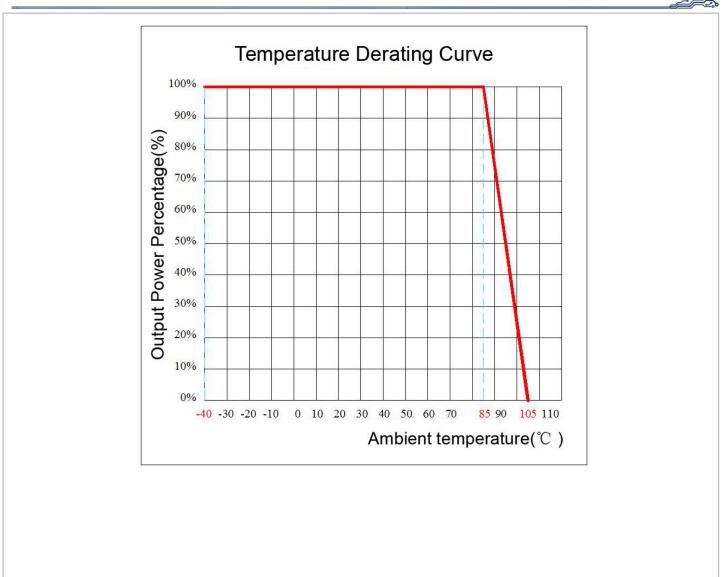




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CE RoHS



Design and Application Circuit Recommended



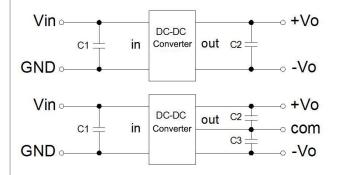
1. Output load requirements

a. In order to ensure the converter can work reliably with high efficiency, the minimum load should not less than 10% rated load when it is used. If the needed power is indeed small, please parallel a resistor at the output side, the resistance equal to 10% nominal load.

b. The maximum capacitive load is tested under nominal input full load, and cannot exceed the maximum capacitive load of output terminal under operation, otherwise it will cause it difficult to start up and damage the product.

2. Recommended circuit

In order to ensure the input/output ripple and noise decreased, capacitor filter net could be connected to input and output terminal, application circuit as below photo 1; choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running safely and reliably, the recommended capacitive load values as shown in Table 1. (But for the actual output power of application circuit is less than 0.5W, suggest not to connect external capacitor)

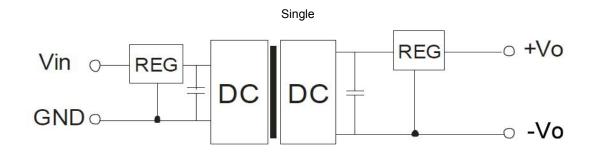


Recommended capacitive load value(Table 1)

Vin (Vdc)	C1 (µF)	Vout (Vdc)	C2 (µF)	Vout (Vdc)	C2,C3 (µF)
3.3/5	4.7	3.3/5	10	±3.3/±5	4.7
12	2.2	9	4.7	±9	2.2
<mark>1</mark> 5	1	12	2.2	±12	1
24	1	15	1	±15	0.47
		24	0.47	±24	0.22

3. Output regulated voltage and over voltage protection circuit

The simplest device to protect output regulated voltage, over voltage and over current is to cascade a linear regulator with overheat protection at input or output terminal, and connect a capacitor filter net(see below picture), filter capacitive value recommended see table 1, Linear regulator is chosen according to the actual voltage, current needed in working, or choose our NW series products.



Note:

1. This product cannot be used in parallel, and do not support hot-plugging;

2.If the product works below the minimum required load, it cannot guarantee that the product performance meets all performance indicators in this manual;

3. All index testing methods in this datasheet are based on our Company's corporate standards

4. The product specification may be changed at any time without prior notice.